

07/28/00



JC890 U.S. PTO

UTILITY PATENT APPLICATION TRANSMITTAL

(Only for new nonprovisional applications
under 37 CFR 1.53(b))

Attorney Docket No. **0100.0001320** Total Pages 28
First Inventor Terry M. LaViolette
Title Method and Apparatus for Posting a Computer
Having Multiple Video Adapters
Express Mail Label No. EL504284831US

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09/628867



07/27/00

APPLICATION ELEMENTS

See MPEP chapter 600 concerning utility patent
application contents

ADDRESS TO:

Assistant Commissioner for Patents
Box Patent Application
Washington, DC 20231

1. ☒ Fee Transmittal Form
(Submit an original, and a duplicate for fee processing)
2. ☒ Specification *Total Pages 16*
(preferred arrangement set forth below)
 - Descriptive title of the Invention
 - Cross References to Related Applications
 - Statement Regarding Fed sponsored R & D
 - Reference to Microfiche Appendix
 - Background of the Invention
 - Brief Summary of the Invention
 - Brief Description of the Drawings (if filed)
 - Detailed Description
 - Claim(s)
 - Abstract of the Disclosure
3. ☒ Drawings (35 USC 113) *Total Sheets 3*
4. Oath or Declaration *Total Pages 3*
 - a. ☒ Newly executed (original or copy)
 - b. ☐ Copy from a prior application
(37 CFR 1.63(d))
(for continuation/divisional with Box 17 completed)
[Note Box 5 below]
 - i. ☐ DELETION OF INVENTOR(S)
Signed statement attached deleting
inventor(s) named in the prior application,
see 37 CFR 1.63(d)(2) and 1.33(b).

6. ☐ Nucleotide and/or Amino Acid Sequence
Submission (if applicable, all necessary)
 - a. ☐ Computer Readable Copy
 - b. ☐ Paper Copy (identical to computer copy)
 - c. ☐ Statement verifying identity of above
copies

ACCOMPANYING APPLICATION PARTS

7. ☒ Assignment Papers (cover sheet & document(s))
8. ☒ 37 CFR 3.73(b) Statement ☒ Power of
(when there is an assignee) Attorney
9. ☐ English Translation Document (if applicable)
10. ☐ Information Disclosure ☐ Copies of
Statement (IDS)/PTO-1449 IDS Citations
11. ☐ Preliminary Amendment
12. ☒ Return Receipt Postcard (MPEP 503)
(Should be specifically itemized)
13. ☐ Small Entity ☐ Statement filed in Prior
Statement(s) Application, Status still
proper and desired.
14. ☐ Certified Copy of Priority Document(s)
(if foreign priority is claimed)
15. ☐ Other

5. ☐ Microfiche Computer Program (Appendix)

16. If a **CONTINUING APPLICATION**, check appropriate box and supply the requisite information:

- ☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application No:
Prior Application Information: Examiner Group / Art Unit.

17. CORRESPONDENCE ADDRESS

☐ Customer Number or Bar Code Label

or, ☒ Correspondence Address Below

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Name (Print/Type)	John R. Garrett	REGISTRATION NUMBER	27,888
Signature		Date	July 27, 2000

FEE TRANSMITTAL

Note Effective October 1, 1997
Patent fees are subject to annual revision

TOTAL AMOUNT OF PAYMENT (\$) 690.00

METHOD OF PAYMENT (check one)

1. ☒ The Commissioner is hereby authorized to charge indicated fees and credit any over payments to Deposit Account Number **50-0441**

Deposit Account Name **ATI Technologies, Inc.**

☒ Charge Any Additional Fee Required Under 37 CFR 1.16 and 1.17

☒ Charge the Issue Fee Set in 37 CFR 1.18 at the mailing of the Notice of Allowance

2. ☐ Payment Enclosed:

☐ Check ☐ Money Order ☐ Other

FEE CALCULATION

1. FILING FEE

Large Entity Fee Code	Small Entity Fee Code	Fee Description	Fee Paid
101 690	201 345	Utility filing fee	690.00
106 310	206 155	Design filing fee	
107 480	207 240	Plant filing fee	
108 760	208 380	Reissue filing fee	
114 150	214 75	Provisional filing fee	

SUBTOTAL (1) (\$) 690.00

2. CLAIMS

Claims	Extra	Fee from below	Fee Paid
Total 20	(20 =)		
Indep 3	(3 =)		
Multiple Dep			

Large Entity Fee Code	Small Entity Fee Code	Fee Description	Fee Paid
103 18	203 9	Claims in excess of 20	
102 78	202 39	Independent claims in excess of 3	
104 260	204 130	Multiple dependent claim	
109 78	209 39	Reissue independent claims over original patent	
110 18	210 9	Reissue claims in excess of 20 and over original patent	

SUBTOTAL (2) (\$) _____

SUBMITTED BY: MARKISON & RECKAMP, P.C.

Typed or Printed Name **John R. Garrett**

Signature *John R. Garrett*

Date **7/29/00**

Complete (if applicable)

Reg. Number **27,888**

Deposit Account User ID **50-0441**

Complete if Known

Application Number	
Filing Date	7/29/00
First Named Inventor	Terry M. LaViolette
Group Art Unit	
Examiner Name	
Attorney Docket Number	0100.0001320

FEE CALCULATION (continued)

3. ADDITIONAL FEES

Large Entity Fee Code	Small Entity Fee Code	Fee Description	Fee Paid
105 130	205 65	Surcharge - late filing fee or oath	
127 50	227 25	Surcharge - late provisional filing fee or cover sheet	
139 130	139 130	Non-English specification	
147 2,520	147 2,520	For filing a request for reexamination	
112 920*	112 920*	Requesting publication of SIR prior to Examiner action	
113 1,840*	113 1,840*	Requesting publication of SIR after Examiner action	
115 110	215 55	Extension for reply within first month	
116 380	216 190	Extension for reply within second month	
117 870	217 435	Extension for reply within third month	
118 1,360	218 680	Extension for reply within fourth month	
128 1,850	228 925	Extension for reply within fifth month	
119 300	219 150	Notice of Appeal	
120 300	220 150	Filing a brief in support of an appeal	
121 260	221 130	Request for oral hearing	
138 1,510	138 1,510	Petition to institute a public use proceeding	
140 110	240 55	Petition to revive - unavoidable	
141 1,210	241 605	Petition to revive - unintentional	
142 1,210	242 605	Utility issue fee (or reissue)	
143 430	243 215	Design issue fee	
144 580	244 290	Plant issue fee	
122 130	122 130	Petitions to the Commissioner	
123 50	123 50	Petitions related to provisional applications	
126 240	126 240	Submission of Information Disclosure Stmt	
581 40	581 40	Recording each patent assignment per property (times number of properties)	40.00
146 690	246 345	Filing a submission after final rejection (37 CFR 1.129(a))	
149 690	249 345	For each additional invention to be examined (37 CFR 1.129(b))	
Other fee (specify)			
Other fee (specify)			

Reduced by Basic Filing Fee Paid

SUBTOTAL (3) (\$) 40.00

**PATENT APPLICATION
DOCKET NO. 0100.0001320**

In the United States Patent and Trademark Office

FILING OF A UNITED STATES PATENT APPLICATION

Title:

METHOD AND APPARATUS FOR POSTING MULTIPLE VIDEO ADAPTERS

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Express Mail Label No. EL504284831US

Date of Deposit, 7/27/00
I hereby certify that this paper is being deposited with the
U.S. Postal Service "Express Mail Post Office to
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D.C. 20231.

Name of Depositor: **Rosalie Swanson**
(print or type)

Signature: Rosalie Swanson

METHOD AND APPARATUS FOR POSTING MULTIPLE VIDEO ADAPTERS

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Field Of The Invention

The present invention relates in general to initialization of computer equipment, and in particular to the POST (power on self test) of multiple video adapters in laptop type computers.

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Background Of The Invention

It is well known in the art that when a typical computer is turned on, the computer executes a self test program referred to as POST. The program is the BIOS (basic input/output system) that is burned into a ROM chip. The BIOS gives the computer a starter mechanism to run the rest of the software from a floppy disk and/or a hard disk. The BIOS is responsible for booting the computer by providing a basic set of instructions. It performs all the tasks that need to be done at start up time. The BIOS also supplies for the operating system an interface to the underlying hardware of the computer in the form of a library of interrupt handlers.

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When the system is powered on, the BIOS performs diagnostics and initializes system components, including the video system. This is commonly referred to as POST. Afterwards, the computer will proceed to its final boot up stage by calling the operating system.

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Computer systems are known in prior art that are able to simultaneously operate a plurality of display devices, such as CRT displays and LCD displays. Such computers may have multiple video adapters or may have a dual CRT Controllers graphics card. The multiple displays are used for simultaneously running multiple applications. Under the Windows 98 operating system this concept is known as extended desktop wherein

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the cursor can drag an icon, file, etc. from the screen of one display device to the screen of another display device. A dual CRT Controllers graphics card is used to operate the two screens of the displays.

5 Under Windows 98 operating systems, it is possible to operate four displays, which, for example, can be run from a first dual CRT Controller graphics card, and the other two from a second dual CRT Controller graphics card. In this case, the computer system has multiple display adapters. Whatever the configuration, it has become an accepted and desired feature for a computer to have the ability to interface with and
10 operate multiple video adapters for simultaneously running multiple display devices.

With the release of the Windows 2000 operating system and especially in the portable or laptop computer environment, it was found that it was no longer possible to support multiple video adapters if any of the video adapters has compressed Video BIOS.
15 For multiple video cards to operate, it is desirable that all of the Video BIOSs be initialized during POST. However, the Windows 2000 operating system is not capable of doing this. Under the Windows 2000 operating system only the primary video adapter is POSTed. Although this primary video adapter could be a dual CRT Controller graphics card for running two display devices, it is not possible to use an add-on video graphics
20 card with an onboard or built in video adapter. Many motherboards for computers have video adapters built into and stored on the motherboard. That is, the video BIOS is stored in compressed form in a ROM chip on the motherboard.

Portable computers, such as laptops, typically have the video BIOS stored in
25 compressed form on the motherboard. If, however, an add-on video card is used, and if this add-on video card is designated as the primary adapter, the video adapter on the motherboard becomes the secondary video adapter and under the Windows 2000 operating system it is no longer available for use after the POST by the System BIOS.

30 There is a need for a method and apparatus for executing a POST for each Video BIOS for a computer in which the computer system is capable of operating multiple

video adapters. That is, there is a need for operating multiple display devices for multiple displays under the Windows 2000 operating system, especially for laptop computers.

Brief Description Of The Drawings

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The features of the present invention which are believed to be novel are set forth with particularity in the appended claims. The invention, together with further objects and advantages, must be understood by reference to the following description taken in conjunction with the accompanying drawings in the several figures of which like

10 reference numerals identify like elements.

FIG. 1 schematically depicts a computer environment for use with the present invention.

FIG. 2 schematically depicts execution of a POST of a computer system using the present invention.

15 FIG. 3 is a flowchart depicting the steps in the method of the present invention.

Detailed Description Of a Preferred Embodiment of The Invention

In the following, the term "secondary adapter" means non-VGA, and the frame
20 buffer may be part of system memory or may be a physically separate memory (ram, harddisk, etc).

Generally, the present invention is a computer system for operating a plurality of display devices. Such a computer system has at least a first video adapter with a first video BIOS and a second video adapter with a second video BIOS. A system BIOS
25 identifies one of the first and second video adapters as a primary video adapter and the other of the first and second video adapters as a secondary video adapter. During POST, the system BIOS stores the first video BIOS in a first memory area in a system memory of the computer. The system BIOS or the Video BIOS then copies the first video BIOS to a second memory area (i.e. frame buffer) when the first video adapter is the secondary
30 video adapter. The system BIOS stores the second video BIOS when the second video adapter is the primary video adapter, and POSTs the second video BIOS in the first

memory area. This then causes the first video BIOS in the first memory area to be overwritten by the second video BIOS. On loading a video driver for the first video adapter, the video driver copies the first video BIOS from the second memory area to a third memory area in the system memory.

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The present invention is particularly useful for operating multiple display devices, such as CRT devices 100 and 102 in FIG. 1. In this computer system, a portable computer or laptop 104 has an LCD display device 106. As is known in the art, the laptop 104 can be inserted into a docking station 108. The laptop 104 interfaces with the
10 docking station 108 through electrical connectors 110. The CRT type display devices 100 and 102 are connected to the docking station 108.

The laptop computer has a motherboard with the first video adapter and has a first video BIOS stored in a ROM on the motherboard. For example, the laptop computer
15 may contain a RAGETM LT PRO, manufactured by ATI Technologies Inc. This is a high performance 2D, 3D, and DVD graphics accelerator for AGP and PCI notebooks. For laptop computers the video BIOS is stored in compressed form. The docking station has in this embodiment an add-on board with the second video adapter and the second video BIOS. Typically, the add-on board has the second video BIOS stored in an
20 uncompressed form. The add-on video card in the docking station 108 typically has an application-specific integrated circuit (ASIC) which is a graphics chip that is able to perform at a higher level than the video adapter on the motherboard of the laptop 104. For example, the video card in the docking station 108 could be a dual CRT Controller video card, which can simultaneously operate both display devices 100 and 102.
25 Alternatively, the docking station could have a video card, such as, the RAGETM XL manufactured by ATI Technologies Inc. This is a high performance video card that provides superior 3D acceleration and comprehensive 3D support.

It is also possible to have a configuration, wherein the laptop computer has a dual
30 display feature. In fact, laptop computers that have the ATI RAGETM MOBILITY 128 graphics chip, manufacture by ATI Technologies Inc., has a Tri-View architecture that

allows the video output to be directed to any three of the LCD, TMDS, CRT and TV displays simultaneously. Dual display makes use of a second, independent controller within the graphics chip to display different images on any two of its supported outputs. This is the feature which is supported by Windows 98 operating systems, but not by

5 Windows 2000 operating systems.

Although under the Windows 2000 operating system it is possible to operate both display devices 100 and 102 with a dual CRT Controller video card in the docking station 108, it would not be possible to simultaneously utilize the LCD screen 106 of the laptop

10 104 with the display devices 100 and 102. This is because, the video BIOS for the video adapter on the motherboard remains compressed and not POSTed and is not available to the video drivers associated with the video adapter on the motherboard. The present invention overcomes this deficiency in the Windows 2000 operating system by providing a modified POST that has minimal impact on the system BIOS and minimal impact on

15 associated video drivers.

FIG. 2 schematically depicts the POST of a computer system which has a motherboard 200 with a ROM 202 which contains the system BIOS 208 and the video BIOS in compressed form for a first video adapter 203. The computer system also has an

20 add-on card 204, which has a ROM 206 that has an uncompressed video BIOS for a video adapter 207 on the video card 204. During POST the System BIOS 208 on the motherboard 200 decompresses the motherboard video BIOS and stores it in a first memory location 210 in system memory 212. When the video adapter 203 on the motherboard 200 is a secondary video adapter and the second video adapter on the add-

25 on video card 204 is the primary video adapter, the System BIOS or Video BIOS 208 then copies the uncompressed and POSTed video BIOS for the first video adapter 203 on the motherboard into second memory area 214 (i.e. frame buffer) .. . Although the uncompressed video BIOS of the motherboard can be copied to other locations in the system memory, the frame buffer is easily addressed by the video system of the

30 computer. The uncompressed video BIOS of the add-on video card 204 is then copied and stored in the first memory area 210, which overwrites the video BIOS of the

motherboard which was stored there. The video driver for the video adapter 203 on the motherboard 200 can now copy the uncompressed Video BIOS from the motherboard from the (second memory area 214 into a third memory area 216 where it can be utilized for ongoing operation of the computer system. It should be understood that the present invention can be extended so that second and third add-on video cards, for example, could be utilized in the computer system. Although the embodiment of the present invention is depicted in the setting of a laptop computer connected to a docking station, other various configurations and combinations of computer equipment and add-on video cards could also be utilized with the present invention.

FIG. 3 is a flow diagram depicting operation of the present invention. In the first step 300 the computer system is initialized by the system BIOS and the power on self test (POST) is executed. Thereafter, in step 302 the video adapters, for example on the motherboard and on the add-on card, are identified as a primary video adapter and a secondary video adapter. In step 304 the first video BIOS is decompressed and stored in a first memory area of the system memory. In step 306, POST occurs for the first video BIOS on the motherboard.

If, in step 308, it is determined that the first video adapter on the motherboard is the primary video adapter, then the POST continues on to finish in step 320. If, however, the first video is the secondary video adapter, then the uncompressed first video BIOS is copied from the first memory area into the second memory area (i.e. frame buffer) in step 312. The frame buffer is the portion of memory reserved for holding the complete bit-mapped image that is sent to the display device. In step 314 the second video BIOS is stored in the first memory area of system memory, thereby overwriting the previously stored first video BIOS. In step 316 the POST operation continues for the second video BIOS on the add-on video card. In step 318, the first video BIOS is copied by the driver from the second memory area, i.e. frame buffer to a third memory area of the system memory for use by the video driver for the first video adapter on the motherboard.

Finally, the POST goes on to finish in step 320.

Thus, the present invention overcomes the deficiency in the prior art (for example, Windows 2000 operating system) by providing a method and apparatus for operating a plurality of display devices with multi-video adapters. It is also accomplished with the least impact on presently existing system BIOS and on associated drivers. The present invention is not limited to the particular details of the apparatus and method depicted and other modifications and applications are contemplated. Certain other changes may be made in the above-described method and apparatus without departing from the true spirit and scope of the invention herein involved. For example, the present invention is not limited to laptop computers, but can be utilized in any type of computer equipment. Also, the present invention could be utilized with operating systems other than the Windows 2000 operating system. It is intended, therefore, that the subject matter of the depicting shall be interpreted as illustrative and not in a limiting sense.

Claims

WHAT IS CLAIMED IS:

- 5 1. A computer system for operating a plurality of display devices, comprising:
 - at least a first video adapter with a first video BIOS and video driver, and
 - a second video adapter with a second video BIOS;
 - a processing unit;
 - a system memory operatively coupled to the processing unit;
 - 10 a read only memory operatively coupled to the processing unit, the read only memory storing a system BIOS that, when read by the processing unit, causes the processing unit to identify one of the first and second video adapters as a primary video adapter and the other of the first and second video adapters as a secondary video adapter;
 - 15 the processing unit, as directed by the system BIOS, executing a POST (power on self test) during which the system BIOS stores the first video BIOS in a first memory area in a system memory of the computer system, and copies the first video BIOS to a second memory area in the system memory, when the first video adapter is the secondary video adapter and the second video adapter is the primary video adapter, thereafter the second video BIOS being stored in the first memory area, thereby overwriting the first video BIOS in the first memory area; and
 - 20 a video driver for the first video adapter, the video driver copying the first video BIOS from the second memory area to a third memory area in the system memory;
 - 25 wherein the computer system uses at least one of the first and second video adapters to display information on at least one display device connected to the computer.
2. The computer system according to claim 1, wherein the computer system further
 - 30 comprises a motherboard, the motherboard having stored thereon the first video BIOS and the first video adapter.

3. The computer system according to claim 2, wherein the computer system further comprises a motherboard, the motherboard having stored thereon the first video BIOS and the first video adapter, wherein the computer system further comprises an add-on board, the add-on board having stored thereon the second video BIOS, wherein the first video BIOS is stored on the motherboard in compressed form, and wherein the first video BIOS is stored in the first memory area in decompressed form.

4. The computer system according to claim 1, wherein the computer system further comprises an add-on board, the add-on board having stored thereon the second video BIOS.

5. The computer system according to claim 1, wherein the second video BIOS is stored in uncompressed form.

6. The computer system according to claim 1, wherein the computer system comprises a portable computer having the first video adapter and a docking station for receiving the portable computer, the docking station having the second video adapter.

7. The computer system according to claim 6, wherein the portable computer has a first display device, and wherein a second display device is operatively connected to the docking station.

8. The computer system according to claim 7, wherein the first video adapter provides display data to the first video device, and wherein the second video adapter provides display data to the second video device.

9. The computer system according to claim 1, wherein the computer system further comprises a frame buffer operatively connected to the processing unit, and wherein the second memory is the frame buffer.

10. A computer system for operating a plurality of display devices, comprising:
 - a motherboard, the motherboard having a first video adapter with a first video BIOS and a video driver, the first video BIOS being stored on the motherboard in compressed form;
 - 5 an add-on board, the add-on board having a second video adapter with a second video BIOS, the first video BIOS being stored in the first memory area in decompressed form;
 - a system memory having a plurality of memory areas;
 - the motherboard of the computer system, having stored thereon a system BIOS that, during a POST of the computer system, identifies the second video adapter as a primary video adapter and the first video adapters as a secondary video adapter;
 - 10 the system BIOS structured to decompress the first video BIOS and storing the decompressed first video BIOS in a first memory area in a system memory of the computer, to copy the decompressed first video BIOS to a second memory area in the system memory, and thereafter to store the second video BIOS in the first memory area, thereby overwriting the first video BIOS in the first memory area;
 - 15 the video driver structured to copy the first video BIOS from the second memory area to a third memory area in the system memory;
 - 20 wherein the computer system uses at least one of the first and second video adapters to display information on at least one display device connected to the computer.
- 25 11. The computer system according to claim 10, wherein the computer system comprises a portable computer containing the motherboard with the first video adapter and a docking station for receiving the portable computer, the docking station containing the add-on board with the second video adapter.

12. The computer system according to claim 11, wherein the portable computer has connected thereto the first display device, and wherein the second display device is operatively connected to the docking station.
- 5 13. The computer system according to claim 10, wherein the first video adapter provides display data to at least the first video device, and wherein the second video adapter provides display data to at least the second video device.

14. A method for posting a computer system having at least a first video adapter with a first video BIOS and a second video adapter with a second video BIOS, comprising the steps of:

initializing a system BIOS of the computer system;

- 5 identifying the second video adapter as a primary video adapter and the first video adapter as a secondary video adapter;

posting the first video BIOS;

storing the first video BIOS in a first memory area in a system memory of the computer;

- 10 copying the first video BIOS to a memory area;

posting the second video BIOS;

storing the second video BIOS in the first memory area, thereby

overwriting the first video BIOS in the first memory area;

loading a video driver for the first video adapter;

- 15 copying the first video BIOS from the video frame buffer area to a further memory area in the system memory;

using at least one of the first and second video adapters to display information on at least one display device connected to the computer.

- 20 15. The method according to claim 14, wherein the computer system comprises a motherboard, the motherboard having stored thereon in compressed form the first video BIOS.

16. The method according to claim 15, wherein the system BIOS, during POST,
25 decompresses first video BIOS, and stores the decompressed first video BIOS in the first memory area of the system memory.

17. The method according to claim 14, wherein the computer system has an add-on board, the add-on board having stored thereon the second video BIOS in
30 uncompressed form.

18. The method according to claim 14, wherein the computer system comprises a portable computer having the first video adapter and a docking station for receiving the portable computer, the docking station having the second video adapter.

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19. The computer system according to claim 18, wherein the portable computer has a first display device, and wherein at least a second display device is operatively connected to the docking station, the first video adapter providing display data to the first video device, and the second video adapter providing display data to the second video device.

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20. The method according to claim 14, wherein the computer system has a plurality of video adapters and respectively associated display devices.

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METHOD AND APPARATUS FOR POSTING A COMPUTER HAVING MULTIPLE VIDEO ADAPTERS

Abstract Of The Invention

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The computer system for operates a plurality of display devices 100 and 102.

Such a computer system has at least a first video adapter 203 with a first video BIOS and a second video adapter 207 with a second video BIOS. A system BIOS identifies one of the first and second video adapters 203, 207 as a primary video adapter and the other of
10 the first and second video adapters 203, 207 as a secondary video adapter. During POST, the system BIOS stores the first video BIOS in a first memory area 210 in a system memory 212 of the computer. The system BIOS or Video BIOS then copies the first video BIOS to a second memory area 214 in the system memory 212 when the first video adapter 203 is the secondary video adapter. The system BIOS POSTs the second video
15 BIOS when the second video adapter 207 is the primary video adapter, and stores the second video BIOS in the first memory area 210. This then causes the first video BIOS in the first memory area 210 to be overwritten by the second video BIOS. On loading a video driver for the first video adapter 203, the video driver copies the first video BIOS from the second memory area 214 to a third memory area 216 in the system memory 212.

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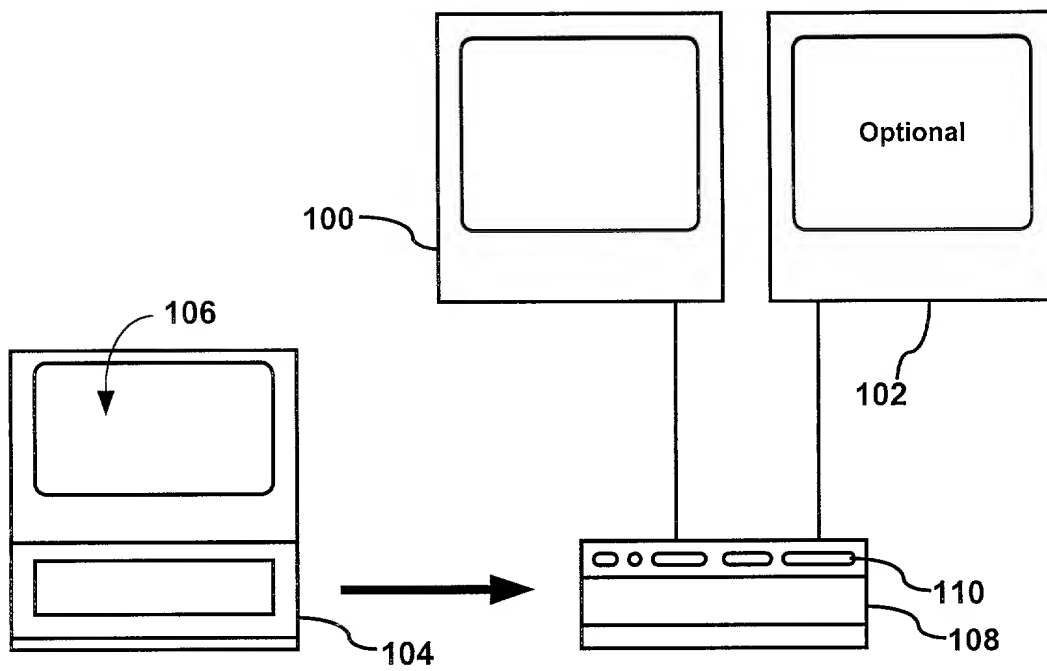


Fig. 1

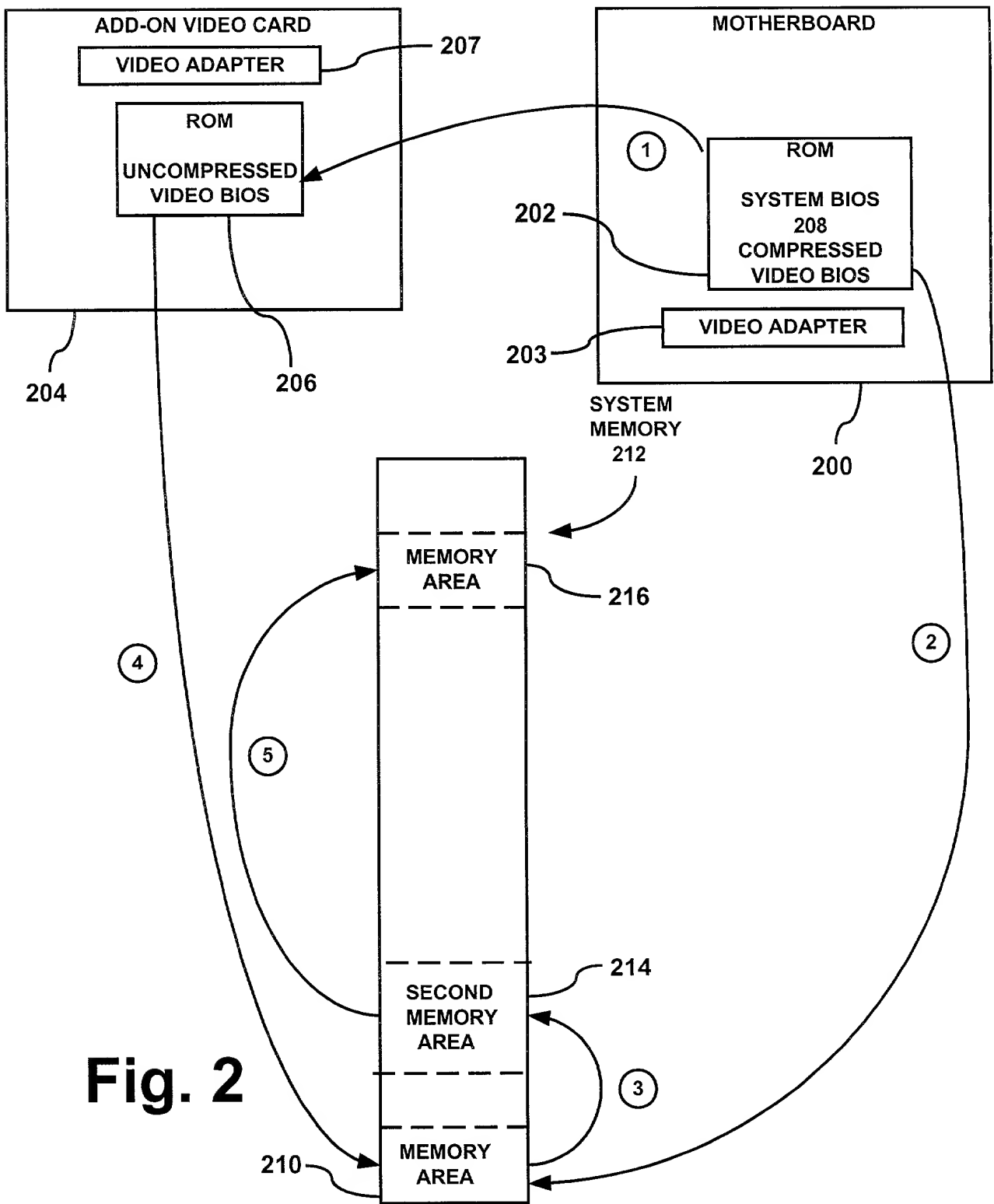


Fig. 2

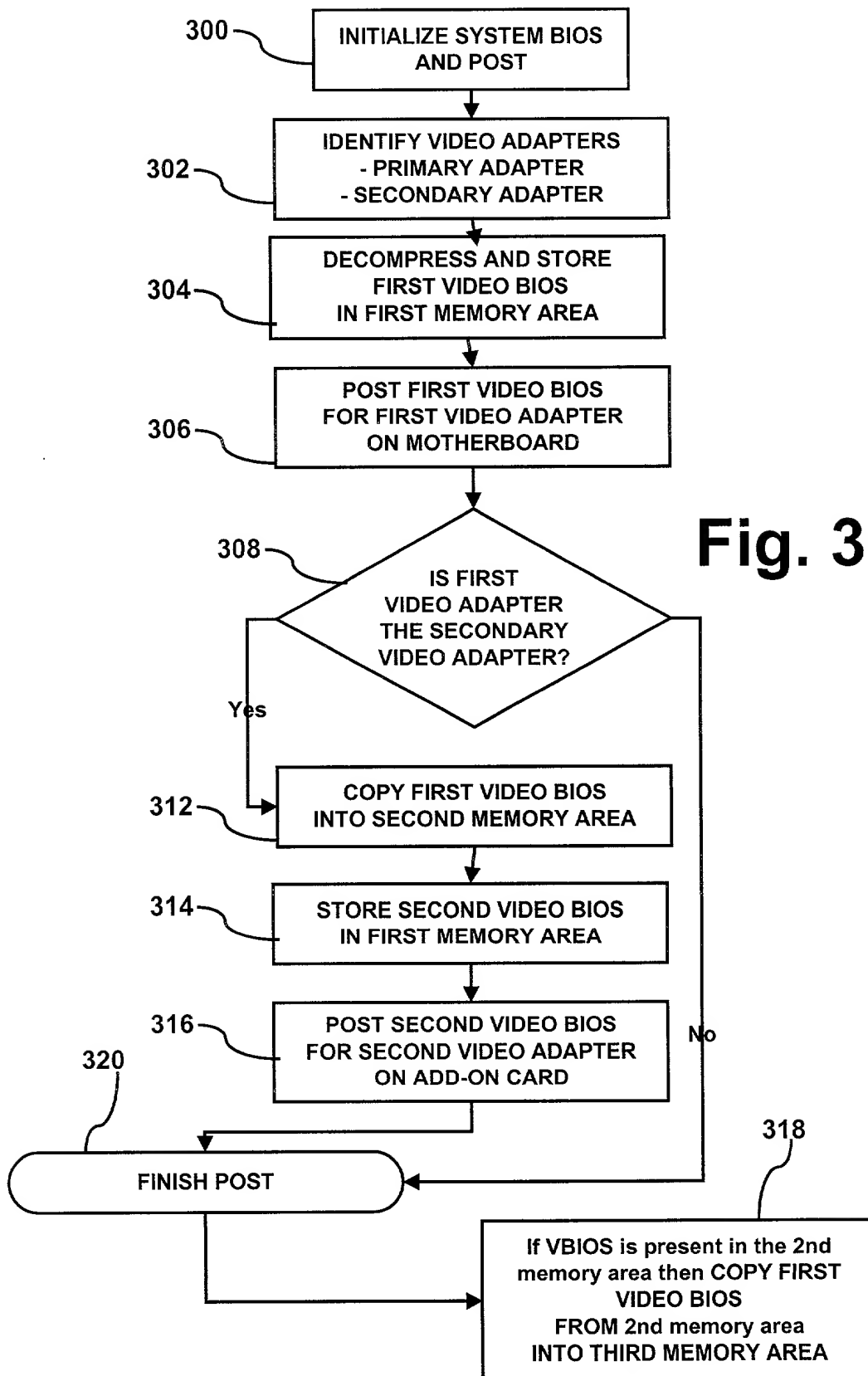


Fig. 3

**DECLARATION
FOR UTILITY OR DESIGN
PATENT APPLICATION
(37 CFR 1.63)**

- ☒ Declaration Submitted with Initial Filing, OR
☐ Declaration Submitted after Initial Filing
(surcharge (37 CFR 1.16 (e)) required)

Attorney Docket Number 0100.0001320
First Named Inventor Terry M. Laviolette
COMPLETE IF KNOWN
Application Number
Filing Date
Group Art Unit
Examiner Name

As a below named inventor, I hereby declare that:

My residence, post office address, and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: **Method and Apparatus for Posting a Computer Having Multiple Video Adapters**
the specification of which:

☒ is attached hereto.

☐ was file on (MM/DD/YYYY) as United States Application Number or PCT International Application Number and was amended on (MM/DD/YYYY) (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment specifically referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. 119(a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or 365(a) of any PCT international application which designated at least one country other than the United States of America, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or of any PCT international application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application Number(s)	Country	Foreign Filing Date (MM/DD/YYYY)	Priority Not Claimed	Certified Copy Attached?	
				YES	NO
			<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

☐ Additional foreign application numbers are listed on a supplemental priority data sheet PTO/SB/02B attached hereto.

I hereby claim the benefit under 35 U.S.C. 119(e) of any United States provisional application(s) listed below.

Application Number(s)	Filing Date (MM/DD/YYYY)

☐ Additional provisional application numbers are listed on a supplemental priority data sheet PTO/SB/02B attached hereto.

I hereby claim the benefit under 35 U.S.C. 120 of any United States application(s), or 365(c) of any PCT international application designating the United States of America, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of 35 U.S.C. 112, I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application.

U.S. Parent Application or PCT Parent Number	Parent Filing Date (MM/DD/YYYY)	Parent Patent Number (if applicable)

☐ Additional U.S. or PCT international application numbers are listed on a supplemental priority data sheet PTO/SB/02B attached hereto.

As a named inventor, I hereby appoint the following registered practitioner(s) to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

Name	Registration Number	Name	Registration Number
John R. Garrett	27,888	Christopher J. Reckamp	34,414
Paul M. Anderson	39,896	Ross D. Snyder	37,730
Sally Daub	41,478		

☐ Additional registered practitioner(s) named on supplemental Registered Practitioner Information sheet PTO/SB/02C attached hereto.

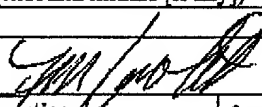
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Markison & Reckamp, P.C.
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Chicago, Illinois 60606-0229
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Facsimile: 312-939-9828

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

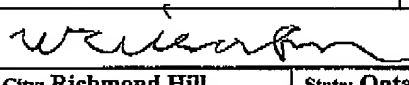
Name of Sole or First Inventor:

☐ A petition has been filed for this unsigned inventor

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Country: Canada			

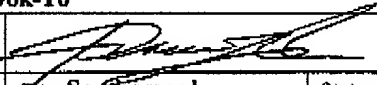
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☐ A petition has been filed for this unsigned inventor

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Country: Canada			

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☒ Additional inventors are being named on the 1 supplemental Additional Inventor(s) sheet(s) PTO/SB/02A attached hereto.



PTO/SB/02A (3-97)

DECLARATION

 ADDITIONAL INVENTOR(S)
 Supplemental Sheet
 Page 1 of 1

Name of Additional Joint Inventor:

☐ A petition has been filed for this unsigned inventor

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Post Office Address			
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Name of Additional Joint Inventor:

☐ A petition has been filed for this unsigned inventor

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Inventor's Signature		Date	
Residence	City:	State:	Country:
Post Office Address			
City:	State:	ZIP:	Country:

Name of Additional Joint Inventor:

☐ A petition has been filed for this unsigned inventor

Given Name (first and middle [if any])		Family Name or Surname	
Inventor's Signature		Date	
Residence	City:	State:	Country:
Post Office Address			
City:	State:	ZIP:	Country:

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